### **A.V.C.** College of Engineering

### Mannampandal, Mayiladuthurai,

### **Department of Electrical and Electronics Engineering**

Title of the Program: Online webinar on "Recent trends in Power systems

Chief Guest details: Mrs. K. Yamuna, Asso. Prof./EEE,KSR College of Engg, Tiruchenkode.

Co-ordinator: Dr.SA.Chithradevi Associate Professor-EEE, AVCCE

**Invitation:** 





### A.V.C. COLLEGE OF ENGINEERING

MANNAMPANDAL, MAYILADUTHURAI

Department of Electrical & Electronics Engineering

### Webinar on

"RECENT TRENDS IN POWER SYSTEMS"

Date: 29.10.21 Friday @ 12.00pm



### Resource Person

Mrs. K. Yamuna
Associate Professor/EEE
K.S.R. College of Engineering, Tiruchengode

With Regards HOD, Staff & Students Department of Electrical & Electronics Engineering

# Product Design and Development AGENDA

Date:29.10.2021 Time: 12.00 PM

Welcome Address	Dr. N.Dhanasekar Professor, Dept of EEE, AVCCE	
Inaugural Address	Dr. SA.Chíthradeví Program Co-ordinator, Associate Professor, Dept of EEE, AVCCE	
Introduction of Chief Guest	<b>Dr.A.Ragavendíran</b> Assocíate Professor Department of EEE, AVCCE	
Chíef Guest Address	Mrs. K. Yamuna Associate Professor Department of EEE, K.S.R. College of Engineering, Trichengode.	
Vote of Thanks	Mr.G.Ashokkumar Assistant Professor Dept of EEE, AVCCE	

### **Program Report:**

The Department of Electrical and Electronics Engineering, hosted a guest lecture programme on the topic of "Recent Trends on power system" on 29.10.2021, from 12:00 PM to 1:30 PM. Guest start the lecture through the online mode and students attend the session in class room of the EEE department. The event commenced with a welcoming message delivered by Dr. N.Dhanasekar, Professor-EEE. This was followed by an inaugural address given by Dr. A. Ravi, a Professor and the Head of the EEE Department. Dr. SA Chithradevi, an associate professor EEE, presented the esteemed chief guest.

In this session Mrs. Yamuna delivered the following points in his lecture.

Power system faults and failures

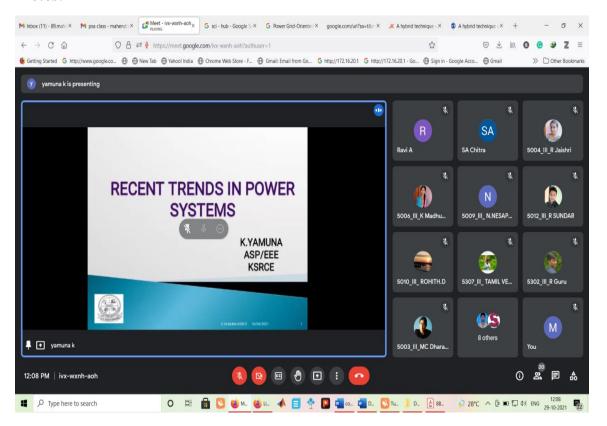
Discussed Difficulties and Mitigating the failures in power system

Specifications of Power system networks, PMU Architecture Hierarchy

Protection schemes by Electromagnetic Relays and future methods.

Softwares used for short circuit fault analysis of a power system network

#### **Photos:**



### **Student Attendance -**

A.V.C COLLEGE OF ENGINEERING

## DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

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